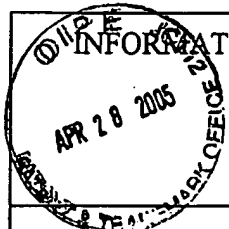


<b>INFORMATION DISCLOSURE STATEMENT</b>				Atty. Docket No. 1765-US		Serial No. <u>10/675083</u>		
				Applicant: XU, Fang				
				Filing Date: Herewith <u>09/30/03</u>		Group: <del>Not Assigned</del> <u>2629</u>		
<b>U.S. PATENT DOCUMENTS</b>								
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if appropriate	
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							Yes	No
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<i>pa</i>	AJ	<b>On the bisection width and expansion of butterfly networks</b> Bornstein, C.; Litman, A.; Maggs, B.M.; Sitaraman, R.K.; Yatzkar, T.; Parallel Processing Symposium, 1998. 1998 IPPS/SPDP. Proceedings of the First Merged International...and Symposium on Parallel and Distributed Processing 1998 , 30 March-3 April 1998 Page(s): 144 -150						
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Examiner <i>[Signature]</i>				Date Considered <u>05/14/05</u>				
Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								

				Atty. Docket No. 1766-US		Serial No. 10/675,083		
				Applicant: Fang Xu				
				Filing Date: 9/30/2003		Group: 2829		
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		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
<i>KL</i>	AG	56117495	9/14/1981	Japan	<del>3164</del>	<del>17100</del> <i>KL</i>	<i>Abstract</i>	X
<i>KL</i>	AH	2 538 912 A	7/6/1984	France				X
	AI							
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)</b>								
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<i>KL</i>	AK	"LOGICALLY CONTROLLED CHIP INTERCONNECTION TECHNIQUE" IBM Technical Disclosure Bulletin, IBM Corp. New York, US - vol. 32, no 3B, 1 August 1989 (1989-08-01), pages 294-299, XP000029849, ISSN: 0018-8689, figure 3						
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Examiner <i>Pureth</i>				Date Considered <i>05/14/05</i>				
Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								